

REMARKS

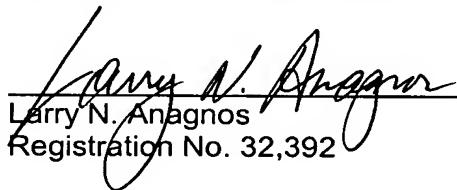
Formal acceptance of the above amendments as a submission in connection with USPTO RCE practice is respectfully requested. (It should be noted, also, an IDS is being filed concurrently herewith as part of this submission.)

The status of the claims are given herein above. The previously allowed claims 16-38 remain as is. However, with regard to claim 16, a discovered typographical error has been corrected. Namely, an inadvertent period was converted to a comma, as was obviously intended. Applicants have added three (3) dependent claims which set forth the size relationship between that of the first semiconductor chip and the second semiconductor chip as well as further characterizing the memory card such that the set forth "second wires" are crossing over one side of the first semiconductor chip, with respect to a plan view thereof. Specifically, newly added dependent claims 39 and 40 further characterize the size relationship between that of the first semiconductor chip and second semiconductor chip with regard to the memory card set forth in claims 16 and 29, respectfully, while new dependent claim 41 further characterizes the memory card according to independent claim 29.

Examination of the above-identified application as further amended hereinabove as well as favorable action on all of the currently pending claims including with regard to the newly added claims is respectfully requested.

Applicants request any shortage or excess in fees in connection with the filing of this paper, including extension of time fees, and for which no other form of payment is offered, be charged or credited to Deposit Account No. 01-2135 (Case: 501.39155VC2).

Respectfully submitted,
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